2001/003

JUN 1 3 2006

SAWYER LAW GROUP LLP

2465 E. BAYSHORE ROAD SUITE 406 PALO ALTO, CA 94303 TELEPHONE: (650) 493-4540 FACSIMILE: (650) 493-4549

FACSIMILE TRANSMITTAL

Date: June 13, 2006

To: Examiner Mathew T. Urick

GROUP ART UNIT: 2113

Organization: United States Patent and Trademark Office

Fax Number: (571) 273-8300

Telephone Number: (571) 272-0805

From: James R. Riegel

Re: USSN 10/609,892

Our Ref.: 2847P

Enclosed is the Applicant Initiated Interview Request Form, along with the Authorization to Act in a Representative Capacity for the above-referenced case.

If you have any questions or need further information, please contact us.

This is page 1 of 3 page(s).

CONFIDENTIALITY NOTE:

The information contained in this facsimile (FAX) message is legally privileged and confidential information intended only for the use of the receiver or firm named above. If the reader of this message is not the intended receiver, you are hereby notified that any dissemination, distribution or copy of this FAX is strictly prohibited. If you have received this FAX in error, please immediately notify the sender at the telephone number provided above, and return the original message to the sender at the address above via the United States Postal Service. Thank you.

RECEIVED CENTRAL FAX CENTER

ER 团 002/003

JUN 1 3 2006

Attorney Docket: RPS920030073US1/2847P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Date: June 13, 2006

Marcus A. BAKER et al.

Confirmation No.: 8340

Application No.: 10/609,892

Group Art Unit: 2113

Filed: June 30, 2003

Examiner: Urick, Matthew T.

For: METHOD AND SYSTEM FOR SAVING THE STATE OF INTEGRATED

CIRCUITS UPON FAILURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AUTHORIZATION TO ACT IN A REPRESENTATIVE CAPACITY

The practitioner named below is authorized to conduct interviews and has the authority to bind the principal concerned. Furthermore, the practitioner is authorized to file correspondence in the above-identified application pursuant to 37 CFR 1.34:

James R. Riegel

Reg. No. 36,651

This is not a power of attorney to the above-named practitioner. Accordingly, the practitioner named above does not have authority to sign a request to change the correspondence address, a request for an express abandonment, a disclaimer, a power of attorney, or other document requiring the signature of the applicant, assignee of the entire interest, or an attorney of record.

Respectfully submitted,

SAWYER LAW GROUP LLP

June 13, 2006

Date

Kelvin M. Vivian
Attornev of Record

Reg. No. 47,797 (650) 493-4540

APPLICANT INITIATED INTERVIEW REQUEST FORM

Application No.: 10/609,892		First Name	First Named Applicant: Baker, Marcus A.			
Examiner: Urick, M	latthew T.	Art Unit: 2	Art Unit: 2113 Status of Application: pending			
Tentative Participants: 1) James Riegel 3)		2) Examin 4)	2) Examiner Urick 4)			
Proposed Date of In	Proposed T	Proposed Time: 2:00 pm EST				
Type of Interview R (1) [X] Telephoni		Personal	(3) [] Video (Conference		
Exhibit To Be Show If yes, provide brief		ted: [] YES	[X] NO			
		ISSUES TO BE	DISCUSSED		•	
Issues (Rej., Obj., etc.)	Claims Fig. #s	Prior Art	Discussed	Agreed	Not Agreed	
(1) <u>102(b) rej.</u>	1, 14, 27	Vachon '404	[]	[]	[]	
(2) <u>103(a) rej.</u>	3,9,11,16,24,29,36	Vachon '404, '348	[]	[]	[]	
(3) 103(a) rej.	7,12,22,34	Vachon '404, Masuva	<u>ma</u> []	[]	[]	
(4)			[]	[]	[]	
[] Continuation Sh	neet Attached					
memory when an unrec stoppedFor 102 Rejection, Va an integrated circuit; ar integrated circuits are n errorFor 103 Rejections: Verror of the integrated c system failure, continue	mending the independence overable error occurrence of the system is still tot stopped. Also, the vachon '348 and Maircuit and when interest operation, and reserved.	ndent claims to include urs in the integrated cir- disclose or suggest this I running after failure, e operating system can assuyama also do not dis- ernal clocks of the integra- suming execution of the	cuit and the internation. Vachon discloses allowing the debugg resume execution, a sclose saving a state ated circuit have be operating system, si	an operating sy ger operation, so nd so has not ex of an integrated en stopped; thes milarly to Vach	nit is saved to a non-volatile integrated circuit have been stem failure, not a failure of that internal clocks of any sperienced an unrecoverable of circuit after unrecoverable or references show operating on '404.	
An interview was co	onducted on the	above-identified app	olication on	•		
MPEP 713.01).	not be deleved from		plicant's failure to	submit a writte	ce of the interview (see on record of this interview, as soon as possible.	
(Applicant/Applicant	nt's Representati	ve Signature)	(Examiner/SPE	E Signature)		